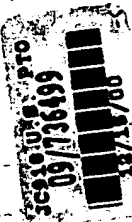


303 896



29	852	Class	Subclass	ISSUE CLASSIFICATION
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PATENT NUMBER

U.S. UTILITY Patent Application

O.I.P.E.	PATENT DATE
TR SCANNED 09/11/99 O.A. JH	

APPLICATION NO. 09/736499	CONT/PRIOR F	CLASS 134	SUBCLASS 852	ART UNIT 372	EXAMINER A. Eley
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TITLE
Method of manufacturing a circuit board and its manufacturing apparatus

PTO-2040
12/89

ISSUING CLASSIFICATION							
ORIGINAL		CROSS REFERENCE(S)					
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)				
29	852	29	853	830			
INTERNATIONAL CLASSIFICATION							
H01K 3/10							

<input type="checkbox"/> TERMINAL DISCLAIMER <input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed. <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. _____ <input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	DRAWINGS Sheets Drwg. 8 Figs. Drwg. 9 Print Fig. 1			CLAIMS ALLOWED Total Claims 50 Print Claim for O.G. 1	
	Timothy V. Eley Primary Examiner (Date) 09/22/03			NOTICE OF ALLOWANCE MAILED 9/23/03 ISSUE FEE Amount Due 1300 Date Paid	
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